



Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	73527	semiconductor and cmos	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/29 14:53
L2	36842	1 and gate and source and drain	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/29 14:53
L3	1518	2 and ferroelectric	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/29 14:54
L4	1187	3 and channel	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/29 14:54
L5	696	4 and (gate adj (insulat\$3 or dielectric))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/29 14:55

Freeform Search

Database:	US Pre-Grant Publication Full-Text Database
	US Patents Full-Text Database
	US OCR Full-Text Database
	EPO Abstracts Database
	JPO Abstracts Database
	Derwent World Patents Index
	IBM Technical Disclosure Bulletins

Term:	semiconductor and cmos and ferroelectric	 
-------	------------------------------------------	----------------------------------------------------------------------------------------------------------------------------------------------------------------------------

Display:	10	Documents in	Display Format:	FULL	Starting with Number	1
----------	----	--------------	-----------------	------	----------------------	---

Generate:	<input type="radio"/> Hit List	<input checked="" type="radio"/> Hit Count	<input type="radio"/> Side by Side	<input type="radio"/> Image
-----------	--------------------------------	--------------------------------------------	------------------------------------	-----------------------------

Search	Clear	Interrupt
--------	-------	-----------

Search History

DATE: Tuesday, March 29, 2005 [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

DB=JPAB; PLUR=YES; OP=ADJ

Hit Count Set Name

result set

<u>L1</u>	semiconductor and cmos and ferroelectric	15	<u>L1</u>
-----------	------------------------------------------	----	-----------

END OF SEARCH HISTORY